

MICRO PROCESSOR

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Inventor(s): NAITO MUTSUHIRO; MIZOGUCHI KAZUAKI

Applicant(s): FUJITSU LTD

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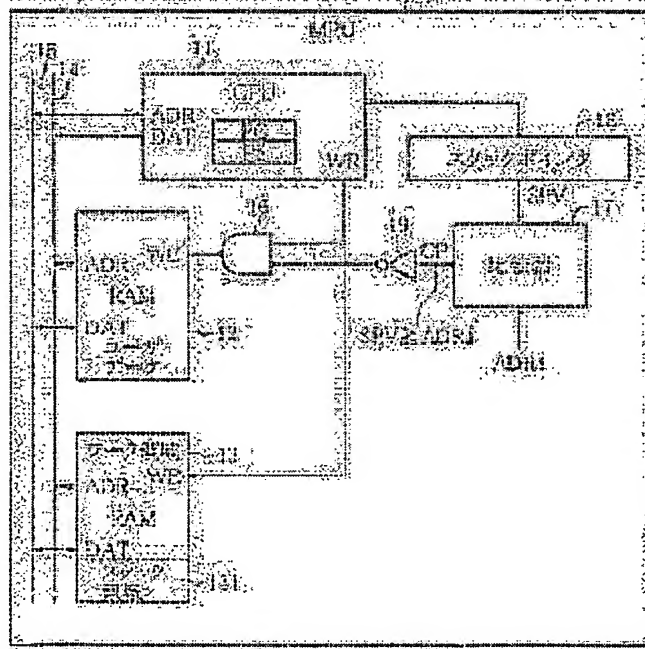
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Abstract of JP 2002073418 (A)

PROBLEM TO BE SOLVED: To provide a low end microprocessor with additional simple hardware that allows an access to a memory and also prohibits the access to the memory. **SOLUTION:** The low end microprocessor 10 has a stack pointer 18, a comparator 17 that compares the pointer contents of SPV with a specified value ADR 1, an inverter 19 that inverts CP as the result of the comparison, and an AND gate 16 that is fed by a write request signal WR from a CPU 11 and an output of the inverter 19. The microprocessor feeds the output of the AND gate 16 into a write enable signal input terminal WE of a memory 12 that can permit or prohibit write operations on a basis of the depth of the stack.



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